

Customer No.: 31561
Application No.: 10/604,173
Docket No.: 10873-US-PA

To the Drawings:

FIG. 2 has been amended to include features such as a plurality of driving stage group 240, as well as the renaming of the driving stages 212, 214, and 232 as the redundant stages 212, 214, 232. No new matter is introduced as a result of the amendment to FIG. 2. FIG. 2 is presented herein as a replacement sheet.

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REMARKS

Present Status of the Application

Claims 1-3 are pending. The Office Action rejected claims 1-3 under 35 U.S.C. 102(b) as being anticipated by Kihara et al. US 5,889,504.

In addition to the traversal of the above claim rejections, new claims 4-14 have been added. No new matter has been introduced as a result of the claim amendments and of the added new claims.

Claim Rejections – 35 U.S.C. § 102

The Office Action rejected claims 1-3 under 35 U.S.C. 102(b) as being anticipated by Kihara et al. US 5,889,504.

In response to the rejection to claims 1-3 under 35 U.S.C. 102(b) as being anticipated by Kihara et al. '504, Applicants have amended claim 1, and hereby otherwise traverse this rejection. Support for the amendments can be found in the specification and the drawings, and particularly in Paragraph 0021 and FIG. 4. As such, Applicant submits that claims 1-3 are now in condition for allowance.

With respect to claim 1, as originally filed, recites in part:

A display driving circuit, comprising:

... a redundant device comprising at least one transistor is installed in each part of the driving stages ...

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Applicants submit that such a display driving circuit as set forth in claim 1, as currently amended, is neither taught, disclosed, nor suggested by Kihara et al. '504 or any of the other cited references, taken alone or in combination.

Kihara et al. '504 fails to disclose, teach or suggest "a redundant device comprising at least one transistor is installed in each part of the driving stages" as set forth in claim 1 (Emphasis added). Accordingly, the present display driving circuit as set forth in claim 1 is new and nonobvious over Kihara et al. '504, or any of the other cited references, taken alone or in combination, and thus should be allowed.

If independent claim 1 is allowable over the prior art of record, then its dependent claims 2 and 3 are allowable as a matter of law, because these dependent claims contain all features of their respective independent claim 1. *In re Fine*, 837 F.2d 1071 (Fed. Cir. 1988).

Kihara et al. '504 clearly teaches that the redundant device of Kihara (e.g. right redundant register capable of supplying an extra conducting path to transmit an electrical signal from the previous driving stage to the next driving stage via the current stage while the original conduct path (e.g. right normal shift register) in the corresponding driving stage is broken. As shown in Figures 4 and 7 of Kihara et al. '504, each (right or left) normal shift register is correspondingly coupled with a (right or left) redundant shift register. However, as described in the invention, when each of the driving stage is added with a redundant device, the circuit fabrication cost is increased and the integration density of the electronic elements is getting higher and higher. The more electronic elements are integrated in the same size of the chip, the greater short problems

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due to too many replacement devices are easily produced. Therefore, the fabrication cost and the impacts of the short circuit and broken circuit are all considered. The present invention provides a configuration that includes a **redundant stage 212, 214 subsequent to N number of the preceding general driving stages**. This configuration could dissolve the previous broken or short problems, and reduce some unnecessary fabrication cost.

Applicants submit that such a display driving circuit as set forth in claims 4 and 10, as currently added, is neither taught, disclosed, nor suggested by Kihara et al. '504 or any of the other cited references, taken alone or in combination.

Kihara et al. '504 fails to disclose, teach or suggest "a redundant stage having a redundant device is installed subsequent to N number of the preceding general driving stages, as set forth in claims 4 and 10. Accordingly, the present display driving circuit as set forth in claims 4 and 10 is new and nonobvious over Kihara et al. '504, or any of the other cited references, taken alone or in combination, and thus should be allowed.

If independent claims 4 and 10 are allowable over the prior art of record, then the dependent claims 5~9 and 11~14 are allowable as a matter of law, because these dependent claims contain all features of their respective independent claims 4 and 10.

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CONCLUSION

For at least the foregoing reasons, it is believed that the pending claims 1-14 are in proper condition for allowance and an action to such effect is earnestly solicited. If the Examiner believes that a telephone conference would expedite the examination of the above-identified patent application, the Examiner is invited to call the undersigned.

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Respectfully submitted,

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